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NAME OF INVENTOR(S): Cheung	
RECEIPT DATE & SERIAL NO.: <b>Serial No.: 09/965,143</b> <b>Filing Date: 9/27/2001</b>	
TITLE OF INVENTION: METHOD AND DEVICE FOR PROVIDING A LOW POWER EMBEDDED SYSTEM BUS ARCHITECTURE	
TI FILE NO.: <b>TI-32390</b>	DEPOSIT ACCT. NO.: <b>20-0668</b>
FAXED: <u>10-28-04</u> DUE: 10/23/2004 ATTY/SEC'Y: WDS/tic	

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Texas Instruments Incorporated  
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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: Cheung  
Serial No: 09/965,143  
Filed: 9/27/2001  
For: METHOD AND DEVICE FOR PROVIDING A LOW POWER EMBEDDED  
SYSTEM BUS ARCHITECTURE

Docket No: TI-32390  
Examiner: Shin, Christopher  
Art Unit: 2182

**AMENDMENT UNDER 37 C.F.R. § 1.115**

Assistant Commissioner For Patents  
Washington, DC 20231

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Tommie Chambers

Dear Sir:

Responsive to the Office Action mailed July 23, 2004 in connection with the  
above identified application, Applicants respectfully submit the following amendments  
and remarks.

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